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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/067,151	01/29/2002	Charle' R. Rupp	STRE-003/01US	4814

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EXAMINER

DO, THUAN V

ART UNIT PAPER NUMBER

2825

DATE MAILED: 04/11/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/067,151

Applicant(s)

RUPP ET AL.

Examiner

Thuan Do

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-- *Th MAILING DATE of this communication appears on the cover sheet with the correspondence address --*
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-5 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-5 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on ____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. ____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892) 4) ☐ Interview Summary (PTO-413) Paper No(s). ____
- 2) ☒ Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) ☐ Notice of Informal Patent Application (PTO-152)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) ____ 6) ☐ Other: _____

DETAILED ACTION

1. Claims 1-5 are pending in this office action.

Claim objection

Claim 5, the terms "a programmable logic adapted " is unclear to what applicant intend to mean. Correction is required.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

(e) the invention was described in-

(1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effect under this subsection of a national application published under section 122(b) only if the international application designating the United States was published under Article 21(2)(a) of such treaty in the English language; or

(2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that a patent shall not be deemed filed in the United States for the purposes of this subsection based on the filing of an international application filed under the treaty defined in section 351(a).

2. Claims 1-3 are rejected under 35 U.S.C. 102(e) as being unpatentable over Mason et al., Pub. No. 2001/0047509 .

Regarding claim 1: Mason teaches a method comprising:

identifying a programmable logic core (page 3, paragraph 0057 for building block of a programmable logic) ;

identifying an application (page 3, paragraph 0057 using interface circuit application);

designing an application specific circuit for the application (page 3, paragraph 0057 using interface circuit for specific application) ; and

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integrating the programmable logic core into the designed application specific circuit (page 2, paragraph 0016 where Mason teaches "merging constraints from one file into another file" to perform the programmable logic integration).

Regarding claim 2: Mason teaches a method comprising:

identifying a programmable logic core for the integrated circuit (page 3, paragraph 0057 for building block of a programmable logic);

establishing a set of timing constraints associated with the programmable logic core (page 3, paragraph 0027) ; and

controlling the design of application specific circuit that interfaces with the programmable logic core in the integrated circuit (page 4, paragraph 0082 where the control can be performed by implementing within programmable logic device) in accordance with the set of timing constraints (page 3, paragraph 0027).

Regarding claim 3: Mason teaches a method comprising:

identifying a programmable logic core for the integrated circuit (page 3, paragraph 0057 for building block of a programmable logic);

establishing a sign-off design associated with the programmable logic core (page 3, paragraph 0057 using a programmable logic sign-off design for an interface circuit); and

controlling the design of application specific circuit that interfaces with the programmable logic core in the integrated circuit (page 4, paragraph 0082) in accordance with the sign-off design (page 3, paragraph 0027).

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 4, 5 are rejected under 35 U.S.C. 102(b) as being unpatentable over DeHon et al., Pat. No. 6,052,773.

Regarding claim 4: DeHon teaches an integrated circuit comprising:

a programmable logic core (col. 3, lines 59-63 where a programmable logic is based on an array of Configurable Arithmetic Logic) ; and

application specific circuitry, the application specific circuitry being designed in accordance with a sign-off design (col. 2, lines 59-67 where a network interface performs specific logic calculation design).

Regarding claim 5: DeHon teaches an integrated circuit including:

a programmable multi-scale array (col. 3, lines 59-63 where a programmable logic is based on an array of Configurable Arithmetic Logic) ;

an application circuit interface for providing a signal interface between the programmable multi-scale array and the application specific circuitry (col. 2, lines 59-67 where a network interface performs data and instruction between components of circuitry) ; and

a programmable logic adapted that configures the programmable multi-scale array (col. 2, lines 59-67 for adapting and interfacing logic design of environment circuits).

CONTACT INFORMATION

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thuan Do whose telephone number is 703-305-2362. The examiner can normally be reached on Monday-Friday 8:30-5:30 (except 2nd Fridays).

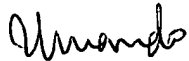
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew S. Smith can be reached on 703-308-1323. The fax phone numbers for the organization where this application or proceeding is assigned are 703305-3431 for regular communications and 703-305-3431 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0596.

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Thuan Do
Patent examiner
4/5/03